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09/970701

		Subclass	ISSUE CLASSIFICATION
		Class	

PATENT NUMBER

U.S. UTILITY PATENT APPLICATION

O.I.P.E.

PATENT DATE

APPLICATION NO. 09/970701	CONT/PRIOR F	CLASS 705	SUBCLASS 26	ART UNIT 2162	EXAMINER HOLZEN
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Shigenobu Maeda

Method for collecting semiconductor devices and method for selling and using semiconductor devices

PTO-2040
12/99

PREPARED AND APPROVED FOR ISSUE

ISSUING CLASSIFICATION

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> a) The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
	(Assistant Examiner) _____ (Date)				
<input type="checkbox"/> b) The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____				ISSUE FEE	
	(Primary Examiner) _____ (Date)			Amount Due	Date Paid
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